IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) A weighting circuit for a receiver which is provided for receiving a multicarrier signal comprising carrier signals, comprising:

~~where the weighting circuit weights the carrier signals such that the spurious signal energy is of equal magnitude for all weighted carrier signals,~~

~~where the weighting circuit has~~ a memory~~, which stores~~ storing a plurality of weighting coefficient sets, ~~and~~

a selector ~~which selects~~ selecting one of the plurality of weighting coefficient sets stored in the memory on the basis of an expected spurious signal energy in the received signal,

~~where the expected spurious signal energy is calculated by~~ an estimation unit calculating said expected spurious signal energy using cross correlation between the received signal and a spurious signal to be expected which has been phase-shifted through 90°;

wherein the weighting circuit weights the carrier signal such that the spurious signal energy is of equal magnitude for all weighted carrier signals; and

wherein said expected spurious signal energy is set externally.

2. (Previously Presented) The weighting circuit as claimed in claim 1, wherein the weighting circuit has at least one multiplier which multiplies an associated

carrier signal by a stored weighting coefficient from the selected weighting coefficient set.

3. (Previously Presented) The weighting circuit as claimed in claim 1, wherein the memory can be programmed via an interface.

4. (Previously Presented) The weighting circuit as claimed in claim 3, wherein the multicarrier signal is broken down into the carrier signals by a computation circuit.

5. (Previously Presented) The weighting circuit as claimed in claim 4, wherein the computation circuit is a Fast Fourier Transformation circuit.

6. (Previously Presented) The weighting circuit as claimed in claim 5, wherein the carrier signals broken down by the computation circuit are buffer-stored in a buffer store.

7-12. (Canceled)